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Amendments to the Claims:

A clean version of the entire set of pending claims, including amendments to the claims, is submitted herewith per 37 CFR 1.121(c)(3). This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Currently Amended) A packet formatter comprising:

a first processing block capable of receiving a dual bitstream signal comprising a standard stream compatible with the Advanced Television Systems Committee (ATSC) standard and a robust stream, the robust stream comprising a plurality of robust packets each having associated therewith header bytes and parity bytes, locations of the parity bytes within each robust packet being dependent upon a position of a currentthe robust packet within a frame of the robust packets and standard packets in the dual bitstream signal;

a second processing block capable of determining the locations of the parity bytes within the current each robust packet according to the current robust packet's position within its the frame, in response to which the first processing block removes the header bytes and parity bytes from the dual bitstream signal to output a first output signal; and

a third processing block capable of receiving said first output signal and removing therefrom duplicate bits associated with said robust stream to thereby produce a second output signal that is output from a data path output of said packet formatter.

2. (Previously Presented) The packet formatter as set forth in claim 1 wherein said packet formatter passes bytes associated with said standard stream to said data path output of said packet formatter after delaying said standard stream bytes by a predetermined delay time.

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3. (Canceled)

4. (Currently Amended) The packet formatter as set forth in claim 1 wherein said second processing block is further capable of determining ~~the~~ locations of said header bytes in said robust stream.

5. (Currently Amended) The packet formatter as set forth in claim 1 wherein said second processing block includes a look-up table which identifies the locations of the parity bytes for each robust packet depending upon the ~~location~~position of the robust packet within the frame.

6. (Previously Presented) The packet formatter as set forth in claim 5 wherein said packet formatter generates and outputs packet identification information used by subsequent processing blocks following said packet formatter.

7. (Previously Presented) A signal comprising the second output signal output from the data path output of the packet formatter as set forth in claim 1.

8. (Currently Amended) For use in a television receiver capable of receiving a dual bitstream signal comprising a standard stream compatible with the Advanced Television Systems Committee (ATSC) standard and a robust stream, the robust stream comprising a plurality of robust packets each having associated therewith header bytes and parity bytes, locations of the parity bytes within each robust packet being dependent upon a position of [[a]] the robust packet within a frame of the robust packets and standard packets in the dual bitstream signal, a method ~~of~~ ~~formatting packets of said dual bitstream signal~~ comprising ~~the steps of~~:
        receiving in a packet formatter said dual bitstream signal;
        determining the locations of the parity bytes within ~~a current~~each robust packet according to the ~~current~~robust packet's position within ~~its~~the frame;
        removing the header bytes and parity bytes from the dual bitstream signal to

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thereby produce a first output signal; and

removing from said first output signal duplicate bits associated with said robust stream to thereby produce a second output signal that is output from a data path output of said packet formatter.

9. (Previously Presented) The method as set forth in claim 8 further comprising the step of delaying bytes associated with said standard stream by a predetermined delay time before outputting said delayed standard stream bytes on said data path output of said packet formatter.

10. (Canceled)

11. (Currently Amended) The method as set forth in claim 8 further comprising the step of determining ~~the~~ locations of the header bytes in said robust stream.

12. (Currently Amended) The method as set forth in claim 8 wherein said step of determining the locations of said parity ~~bits~~ bytes comprises the step of determining the locations of said parity ~~bits~~ bytes from a look-up table which identifies the locations of the parity bytes for each robust packet depending upon the position of the robust packet within the frame.

13. (Previously Presented) The method as set forth in claim 12 further comprising the steps of generating and outputting packet identification information used by subsequent processing blocks following said packet formatter.

14. (Canceled)

15. (Currently Amended) A television receiver comprising:

receiver front-end circuitry capable of receiving and down-converting a dual bitstream signal comprising a standard stream compatible with the Advanced

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Television Systems Committee (ATSC) standard and a robust stream comprising a plurality of robust packets each having associated therewith header bytes and parity bytes, locations of the parity bytes within each robust packet being dependent upon a position of a current the robust packet within a frame of the robust packets and standard packets in the dual bitstream signal, the receiver front-end circuitry producing a baseband signal; and

a forward error correction section capable of receiving said baseband signal from said receiver front-end circuitry, said forward error correction section comprising:

_____a packet formatter comprising:

a first processing block capable of receiving said standard stream and said robust stream associated with said baseband signal,[[;]]

a second processing block capable of determining the locations of the parity bytes within the current each robust packet according to the current robust packet's position within its the frame, in response to which the first processing block removes from the header bytes and parity bytes from the dual bitstream signal to output a first output signal,[[;]] and

a third processing block capable of receiving said first output signal and removing therefrom duplicate bits associated with said robust stream to thereby produce a second output signal that is output from a data path output of said packet formatter;

a robust deinterleaver capable of receiving the second output signal, and deinterleaving data in the robust stream to output a third output signal;

a Reed-Solomon decider decoder capable of receiving the third output signal, and decoding data in the third output signal to output a fourth output signal; and

a derandomizer capable of receiving the fourth output signal and derandomizing bytes associated with said standard stream and bytes associated with said robust stream.

16. (Previously Presented) The television receiver as set forth in claim 15

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wherein said packet formatter passes bytes associated with said standard stream to said data path output of said packet formatter after delaying said standard stream bytes by a predetermined delay time.

17. (Canceled)

18. (Previously Presented) The television receiver as set forth in claim 15 wherein said second processing block is further capable of determining the locations of said header bytes in said robust stream.

19. (Currently Amended) The television receiver as set forth in claim 18 wherein said second processing block comprises a look-up table which identifies the locations of the parity bytes for each robust packet depending upon the position of the robust packet within the frame.

20. (Previously Presented) The television receiver as set forth in claim 19 wherein said packet formatter generates and outputs packet identification information used by subsequent processing blocks following said packet formatter.

21. (Canceled)

22. (Currently Amended) A data de-randomizer for use in a television receiver capable of receiving a dual bitstream signal comprising a standard stream compatible with the Advanced Television Systems Committee (ATSC) standard and a robust stream, said data de-randomizer comprising:
        a standard de-randomizer capable of de-randomizing bytes associated with said standard stream; and
        a robust de-randomizer capable of de-randomizing bytes associated with said robust stream,
        wherein said data de-randomizer further comprises a delay calculation circuit

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for determining a delay associated with the robust stream with respect to a field synchronization signal associated with the robust stream, and applying a control signal to the robust de-randomizer to cause the robust de-randomizer to suspend its operation for a portion of a field in accordance with the determined delay.

23. (New) The packet formatter of claim 1, wherein locations of the parity bytes within a first one of the robust packets within the frame are different than locations of the parity bytes within a second one of the robust packets within the frame, and wherein the second processing block is capable of determining the locations of the parity bytes within each of the first and second robust packets according to the first and second robust packets' corresponding positions within the frame.

24. (New) The method of claim 8, wherein locations of the parity bytes within a first one of the robust packets within the frame are different than locations of the parity bytes within a second one of the robust packets within the frame, and wherein determining the locations of the parity bytes within each robust packet according to the robust packet's position within the frame comprises determining the locations of the parity bytes within each of the first and second robust packets according to the first and second robust packets' corresponding positions within the frame.

25. (New) The television receiver of claim 15, wherein locations of the parity bytes within a first one of the robust packets within the frame are different than locations of the parity bytes within a second one of the robust packets within the frame, and wherein the second processing block is capable of determining the locations of the parity bytes within each of the first and second robust packets according to the first and second robust packets' corresponding positions within the frame.

26. (New) The data de-randomizer of claim 22, further comprising a

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multiplexer receiving de-randomized bytes associated with said standard stream from the standard de-randomizer, and receiving de-randomized bytes associated with said robust stream from the robust de-randomizer, and multiplexing the de-randomized bytes associated with said standard stream from the standard de-randomizer with the de-randomized bytes associated with said robust stream from the robust de-randomizer.

27. (New) The data de-randomizer of claim 22, wherein the delay calculation circuit includes a look-up table storing values that are used to determine the delay.